

ACRONYMS AND ABBREVIATIONS

ACHI	application channel interface
ACP	alarm control process
AJT	active job table
ALU	arithmetic logic unit
ALW	allow
API	attached processor interface
APS	Attached Processor System
ATB	address translation buffer
ATP	all tests passed
BGB	bidirectional gating bus
BIC	bus interface controller
BLDG PWR	building power
BPI	bits per inch
BS	bus simulator
BWM	broadcast warning message
CAD	computer-aided design
CC	central control
CCIO	central control input/output
CCIS	common channel interoffice signaling
CCITT	International Telegraph and Telephone Consultative Committee
CH	channel
CHAN	channel
CIA	critical indicator area
CIH	craft interface handler
CMS	Change Management System
CONFIG	configuration management program
CPD	common processor display
CPH	communication protocol handler
CPU	central processing unit
CRT	cathode ray tube
CSU	cache store unit
CU	control unit
DAP	display administration process
DATA TABLE	diagnostic data table files
DBEVOL	Data Base Evolution System
DBS	duplex bus selector
DCB	diagnostic control block
DCT	dispatch control table
DDCMP	digital data communication message protocol
DDL	data definition language

DDLP	data definition language processor
DDSBS	duplex dual-serial bus selector
DEV	device
DFC	disk file controller
DFDIAG	disk file diagnose
DFI	disk file inverter
DGN	diagnose
DIAGC	diagnostic control
DIAMON	diagnostic monitor
DIO	DMA I/O bus
DIP	dual in-line package
DMA	direct memory access
DMAC	direct memory access controller
DMERT	Duplex Multiple Environment Real Time
DML	data manipulation language
DMU	data manipulation unit
DPT	diagnostic phase table
DRAM	dynamic random access memory
DSCH	dual serial channel
DST	destination
DUC	dual-access utility circuit
DUI	direct user interface
EAI	emergency action interface
ECC	error correction code
ECD	equipment configuration data or data base
ECDMAN	equipment configuration database manager
EIH	error interrupt handler
EOS	extended operating system
EPROM	erasable programmable read-only memory
ER	error register
ESS	Electronic Switching System
EX	exercise
FIFO	first in-first out
FIST	factory/installation software test
FPS	form processing system
FST	factory system tests
FTAM	forms translation and mapping
FTS	field test set
GRASP	generic access package
IB	instruction buffer
INH	inhibit
IODRV	IOP driver process
IOP	input/output processor
IOPC	input/output processor controller

IPC	interprocess communication
IPS	inches per second
KPCB	kernel process control block
LC	line controller
MSGs	messages
MSI	medium-scale integration
MTC	maintenance
MTTR	mean time to repair
MTTY	maintenance TTY
MTTYC	maintenance terminal controller
MU	mask unit
MUX	multiplexor
NCP	network control point
NK	non-killable
NRZ	non-return to zero
ODIN	on-line data integrity
OOS	out of service
OP	display
OST	operating system trap
PA	program address
PC	peripheral controller
PCB	process control block
PCSD	peripheral controller subdevice
PCU	power control unit
PD	peripheral device
PDF	page description file
PDFGEN	PDF generator
PDS	program documentation standard
PE	phase encoded
PFC	peripheral frame control
PIC	peripheral interface controller
PID	process identifier
PINIT	processor initialization program
PRM	processor recovery message
PROM	programmable read-only memory
PSBR	primary segment base register
PSDC	parallel serial data interface
PSI	peripheral system interface
PSW	program status word
QA	quality assurance
RAM	random access memory
RC/V	recent change/verify
RFI	radio frequency interference
RMU	rotate mask unit

RMV	remove
ROP	read-only printer
ROP	receive-only printer
RST	restore
RSTU	restore unconditional
RTS	real-time status
RU	rotate unit
SAC	store address control/controller
SAG	system analysis group
SAR	store address register
SAT	store address translator
SBR	segment base register
SC/SD	scanner/signal distributor
SC	software control
SCANS	Software Change and Notification System
SCC	switching control center
SCCS	Source Code Control System
SCCS	Switching Control Center System
SCH	serial channel
SCM	store complete signal
SCR	silicon controlled rectifier
SCR	store control register
SCSD	scanner and signal distributor
SD	software development
SDC	store data controller
SDP	software demand paging
SDR	store data register
SDS	Software Development System
SG	system generation data base
SGO	store go signal
SGS	Software Generating System
SID	segment identifier
SIM	system integrity monitor
SIR	store instruction register
SMD	storage module drive
SP	software production
SRC	source
SREG	special registers
SSA	system summary area
SSBR	secondary segment base register
SSI	small-scale integration
SSR	system status register
STOP	terminate
SUPR	system update program

SYSGEN	system generator/generation
TB/IS	transaction block and integrity subsystem
TLDB	trouble locating data base
TLP	trouble locating process
TSPS	Traffic Service Position System
TTL	transistor-transistor logic
TTY	teletypewriter or terminal
TTYC	terminal controller
TUS	Test Utility System
TV	transfer vector
UC	utility circuit
UID	utility identifier
USP	<i>UNIX</i> supervisor process
VLSI	very large-scale integration
VOH	voltage output high
VOL	voltage output low
VTOC	volume table of contents
WCS	writable control store
WMS	writable microstore
YACC	yet another compiler compiler

